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Dated: March 22, 2006

Signature:

(Andrew T. Zidel)

Docket No.: SCEI 3.0-058  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Suzuki et al.

Application No.: 09/816,752

Group Art Unit: 2154

Filed: March 22, 2001

Examiner: J. Chang

For: PROCESSING MODULES FOR COMPUTER  
ARCHITECTURE FOR BROADBAND  
NETWORKS

Commissioner for Patents  
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**INFORMATION DISCLOSURE STATEMENT**

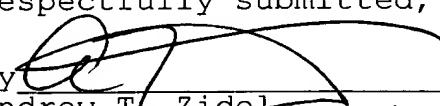
Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

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Dated: March 22, 2006

Respectfully submitted,

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Substitute for form 1449A/B/PTO				Complete if Known	
				Application Number	09/816,752-Conf. #6544
				Filing Date	March 22, 2001
				First Named Inventor	Masakazu Suzuki
				Art Unit	2154
				Examiner Name	J. Chang
Sheet	1	of	1	Attorney Docket Number	SCEI 3.0-058

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA*	US-5,519,875	05-21-1996	Yokoyama et al.		
AB*	US-6,192,514	02-20-2001	Lurndal		
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AJ*	US-20020016863	02-07-2002	LURNDAL		

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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